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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/076,357	02/14/2002	Dwip N. Banerjee	AUS920010893US1	1515

7590 06/29/2005
Mr. Volel Emile
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Austin, TX 78720-2170

EXAMINER

NGUYEN, VAN KIM T

ART UNIT	PAPER NUMBER
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2151

DATE MAILED: 06/29/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/076,357

Applicant(s)

BANERJEE ET AL

Examiner

Van Kim T. Nguyen

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on February 14, 2002.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-20 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-20 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Drawings

1. The drawings are objected to because the unlabeled boxes shown in Figure 5 should be provided with descriptive text labels.

Figures 1 and 4 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g).

The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they include the following reference character not mentioned in the description: Figure 4, ref. 432.

Corrected drawing sheets in compliance with 37 CFR 1.121(d), or amendment to the specification to add the reference character in the description in compliance with 37 CFR 1.121(b) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

3. Claims 1, 6, 11, and 16 are rejected under 35 U.S.C. 102(e) as being anticipated by Potter (US 6,505,269).

As shown in Figures 1-9, Potter discloses a method of transacting network data, comprising:

determining whether data being processed data is network data (i.e., data are processed and transferred between end stations 102, 112, and intermediate station 200, thus the data is network data; col. 4: lines 38-56); and

transacting, if the data is network data, the data using a virtual IP address (if the data is network data, route data using an IP address, e.g., destination IP address; col. 4: line 57 – col. 5: line 65).

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

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(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 2-5, 7-10, 12-15, and 17-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Potter, in view of Fesas, Jr. (US 6,397,316), hereinafter Fesas.

Regarding claims 2, 7, 12, and 17, Potter discloses substantially all the claimed information, except the virtual IP address is an address buffer.

As shown in Figures 1, Fesas teaches indexing buffer addresses within the controller 100 (col. 6: lines 48-52).

Since Potter and Fesas teach analogous arts, relating to transferring network data using improved virtual-to-physical address translation techniques, it would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize Fesas' addressing scheme in Potter's system, motivated by the need to reduce performance bottleneck and thus, improve data transmission quality and rate.

Regarding claims 3, 8, 13, and 18, the combination of Potter and Fesas also discloses the buffer (210, 220, 230, 400) is implemented using memory allocation (Potter: col. 5: lines 16-19; col. 5: lines 30-36; and col. 8: line 60 – col. 9: line 8).

Since Potter and Fesas teach analogous arts, relating to transferring network data using improved virtual-to-physical address translation techniques, it would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize Fesas' addressing scheme

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in Potter's system, motivated by the need to reduce performance bottleneck and thus, improve data transmission quality and rate.

Regarding claims 4, 9, 14, and 19, the combination of Potter and Fesas also discloses the buffer contends for access to one of the limited physical interfaces (e.g., the memory arrays operate independently to avoid contention among the controllers; Potter: col. 7: lines 34-45).

Since Potter and Fesas teach analogous arts, relating to transferring network data using improved virtual-to-physical address translation techniques, it would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize Fesas' addressing scheme in Potter's system, motivated by the need to reduce performance bottleneck and thus, improve data transmission quality and rate.

Regarding claims 5, 10, 15, and 20, the combination of Potter and Fesas also discloses before transmitting the data to the physical interface, the virtual IP address replaced by a destination IP address (e.g., forwarding information based on destination IP address; Potter: col. 5: lines 49-65).

Since Potter and Fesas teach analogous arts, relating to transferring network data using improved virtual-to-physical address translation techniques, it would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize Fesas' addressing scheme in Potter's system, motivated by the need to reduce performance bottleneck and thus, improve data transmission quality and rate.

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Conclusion

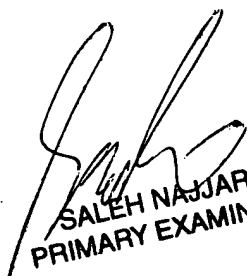
6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Van Kim T. Nguyen whose telephone number is 571-272-3073. The examiner can normally be reached on 8:00 AM - 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Zarni Maung, can be reached on 571-272-3939. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Van Kim T. Nguyen
Examiner
Art Unit 2151

vkn


SALEH NAJJAR
PRIMARY EXAMINER